

## EAST Search History *(Interference Search)*

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	1	((memor\$2 or RAM\$1 or SDRAM\$1 or DRAM\$1 or storage or buffer\$1) and (application\$1 adj4 (directory or list\$1))and (processor\$1 or microprocessor\$1 or micro-processor\$1 or controller\$1 or microcontroller\$1 or micro-controller\$1) and (ad hoc or (ad adj hoc)) and (priorit\$4 adj5 application\$1) and download\$4 and (parameter\$1 or variable\$1)). clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:27
L10	1	((memor\$2 or RAM\$1 or SDRAM\$1 or DRAM\$1 or storage or buffer\$1) and (application\$1 adj4 (directory or list\$1))and (processor\$1 or microprocessor\$1 or micro-processor\$1 or controller\$1 or microcontroller\$1 or micro-controller\$1) and (ad hoc or (ad adj hoc)) and (priorit\$4 adj5 application\$1) and download\$4 and (parameter\$1 or variable\$1)). clm.	US-PGPUB	OR	ON	2007/08/22 19:27
L11	0	((memor\$2 or RAM\$1 or SDRAM\$1 or DRAM\$1 or storage or buffer\$1) and (application\$1 adj4 (directory or list\$1))and (processor\$1 or microprocessor\$1 or micro-processor\$1 or controller\$1 or microcontroller\$1 or micro-controller\$1) and (ad hoc or (ad adj hoc)) and (priorit\$4 adj5 application\$1) and download\$4 and (parameter\$1 or variable\$1) and auto\$8 and (state\$1 or status) and set\$6).clm.	US-PGPUB	OR	ON	2007/08/22 19:29

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# EAST Search History

(update)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	15	(memor\$2 or RAM\$1 or SDRAM\$1 or DRAM\$1 or storage or buffer\$1) and (processor\$1 or microprocessor\$1 or micro-processor\$1 or controller\$1 or microcontroller\$1 or micro-controller\$1) and (application adj (directory or list)) and (adhoc or (ad adj hoc)) and (priority with application\$1) and download\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:13
L3	488	((memor\$2 or RAM\$1 or SDRAM\$1 or DRAM\$1 or storage or buffer\$1) with application\$1) and (processor\$1 or microprocessor\$1 or micro-processor\$1 or controller\$1 or microcontroller\$1 or micro-controller\$1) and (adhoc or (ad adj hoc)) and (priority with application\$1) and download\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:15
L4	1	3 and (application near3 (state\$1 or status or set\$4 or set-up)) and (auto adj launch\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:20
L6	146	3 and (application with (state\$1 or status)) and (application with (set\$4 or set-up)) and auto\$8	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:22
L7	28	6 and ("370"/\$.ccls. or "455"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:23
L8	10	3 and (370/338.ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/08/22 19:24

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